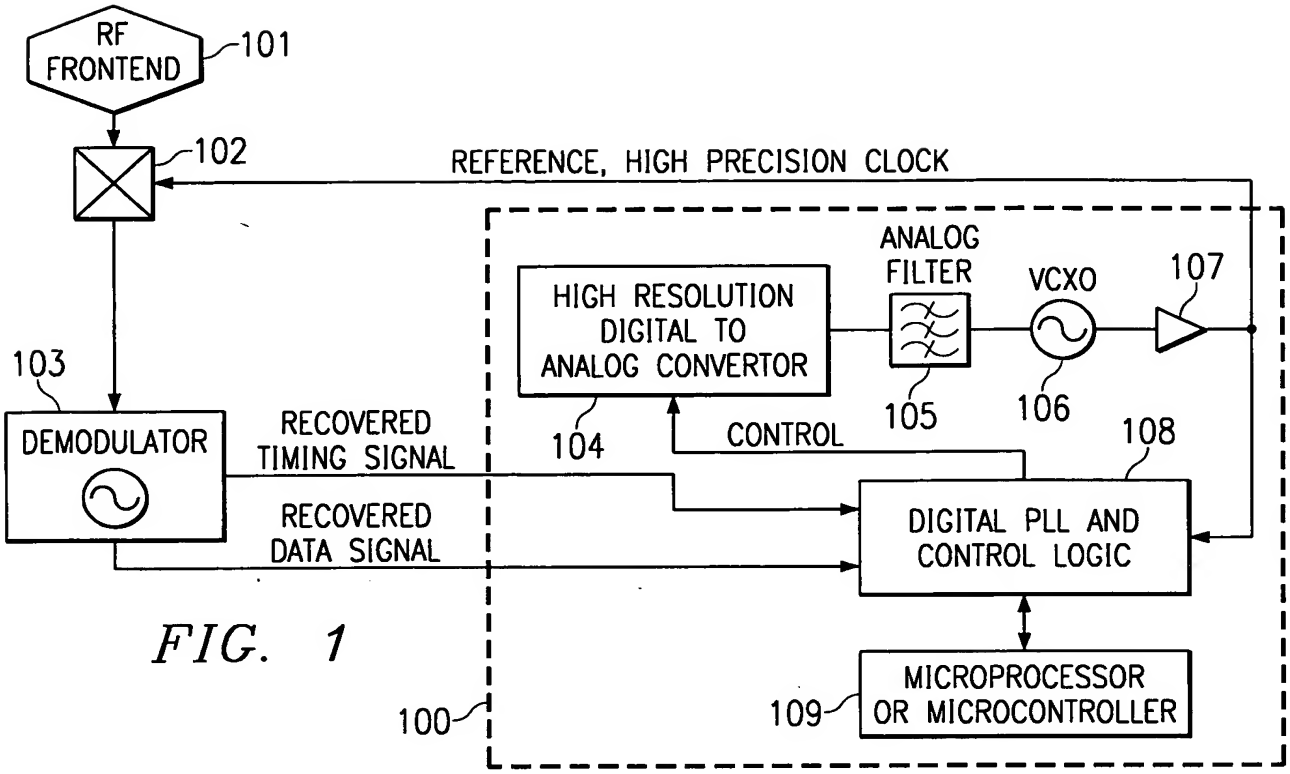

FIG. 1

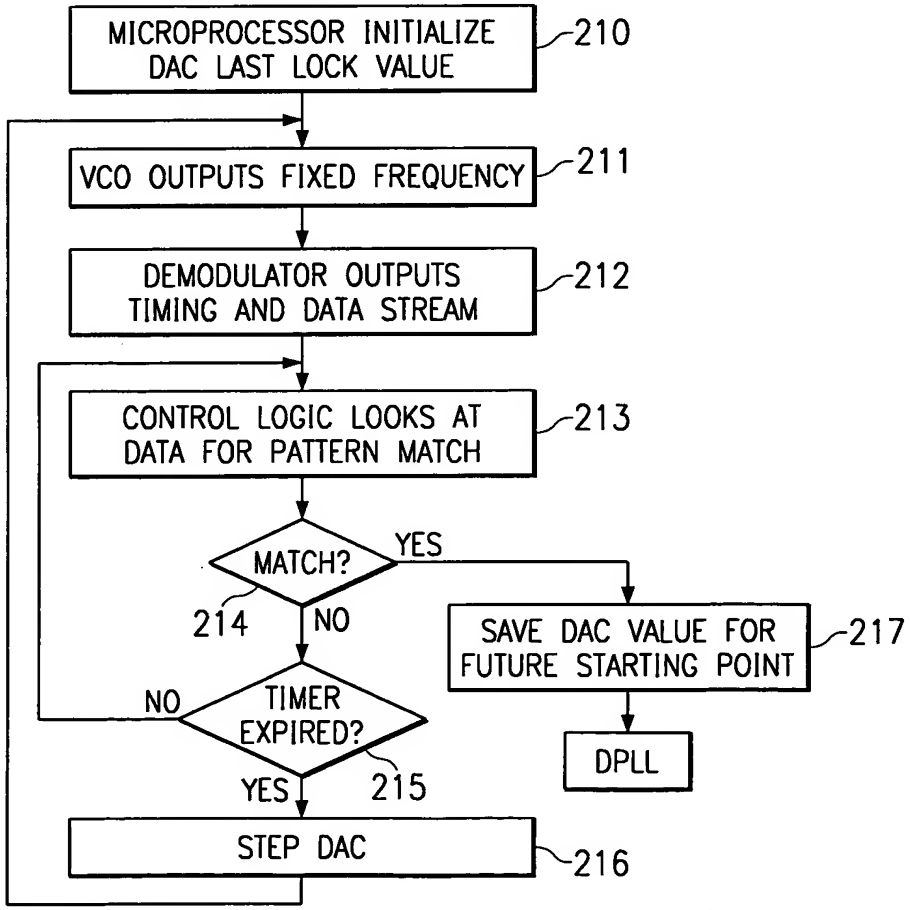

FIG. 2

BEST AVAILABLE COPY

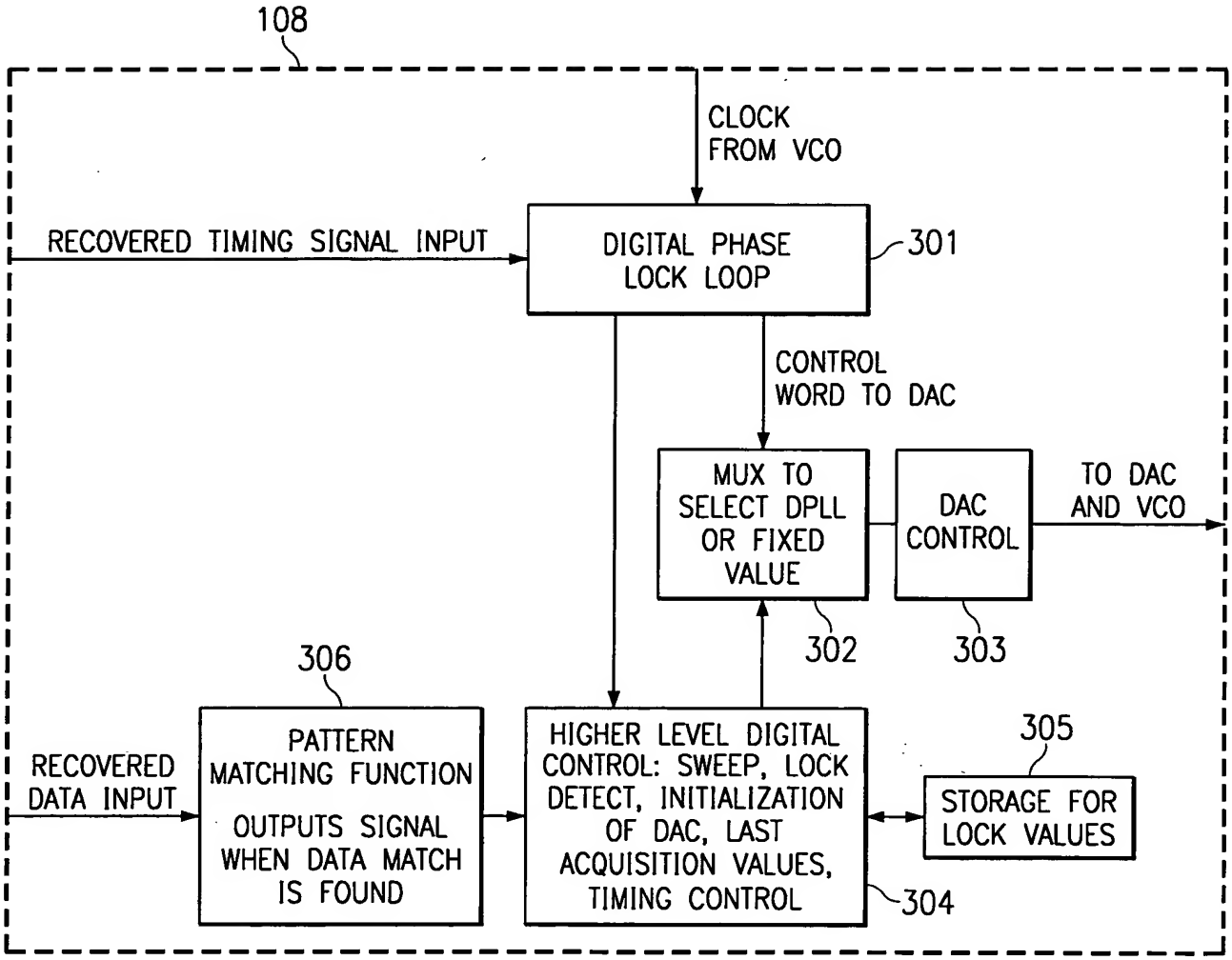
108
CLOCK FROM VCO
RECOVERED TIMING SIGNAL INPUT
DIGITAL PHASE LOCK LOOP
301
CONTROL WORD TO DAC
MUX TO SELECT DPLL OR FIXED VALUE
DAC CONTROL
TO DAC AND VCO
302
303
306
PATTERN MATCHING FUNCTION
OUTPUTS SIGNAL WHEN DATA MATCH IS FOUND
RECOVERED DATA INPUT
HIGHER LEVEL DIGITAL CONTROL: SWEEP, LOCK DETECT, INITIALIZATION OF DAC, LAST ACQUISITION VALUES, TIMING CONTROL
304
305
STORAGE FOR LOCK VALUES

FIG. 3